

Fig. 1

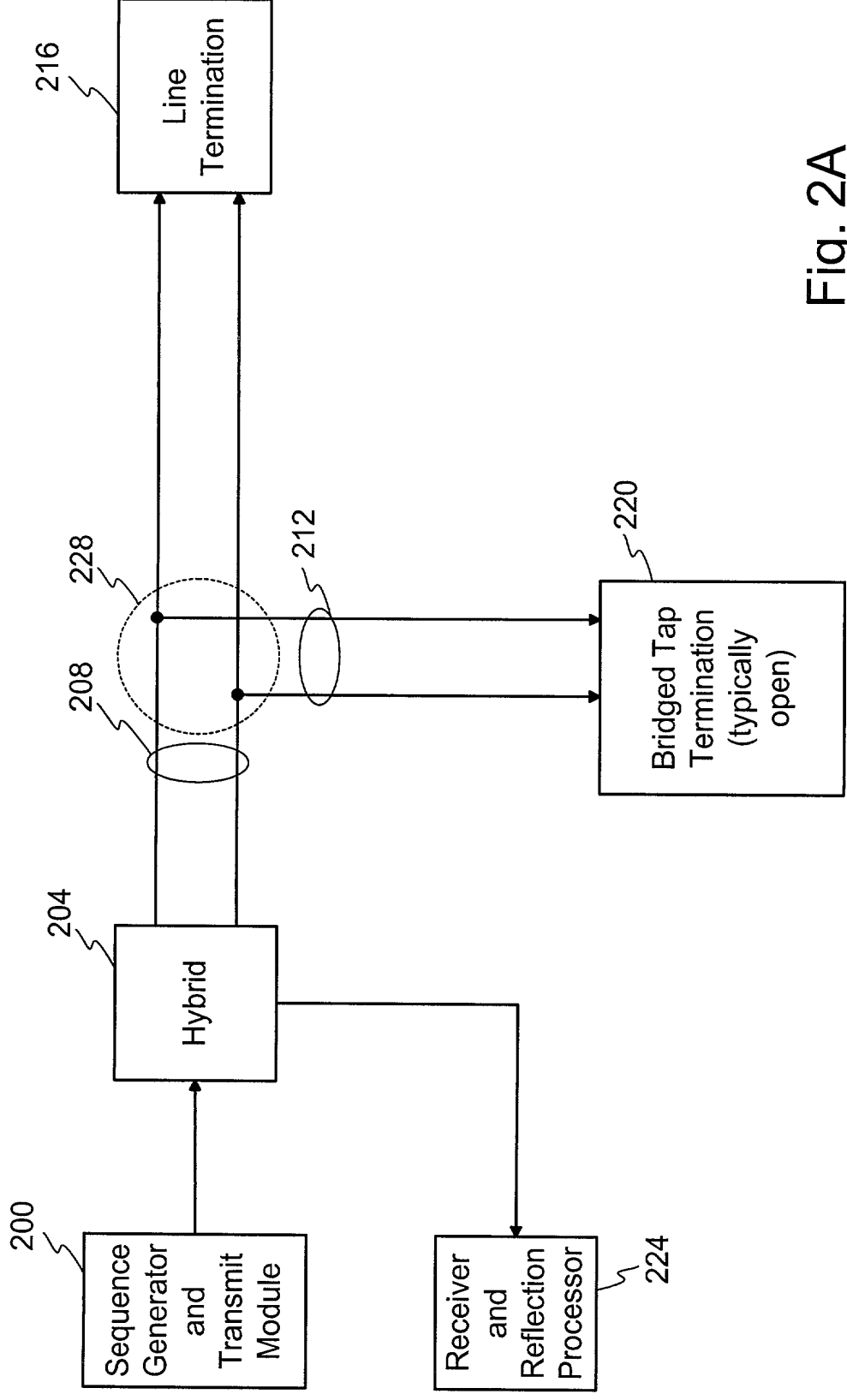


Fig. 2A

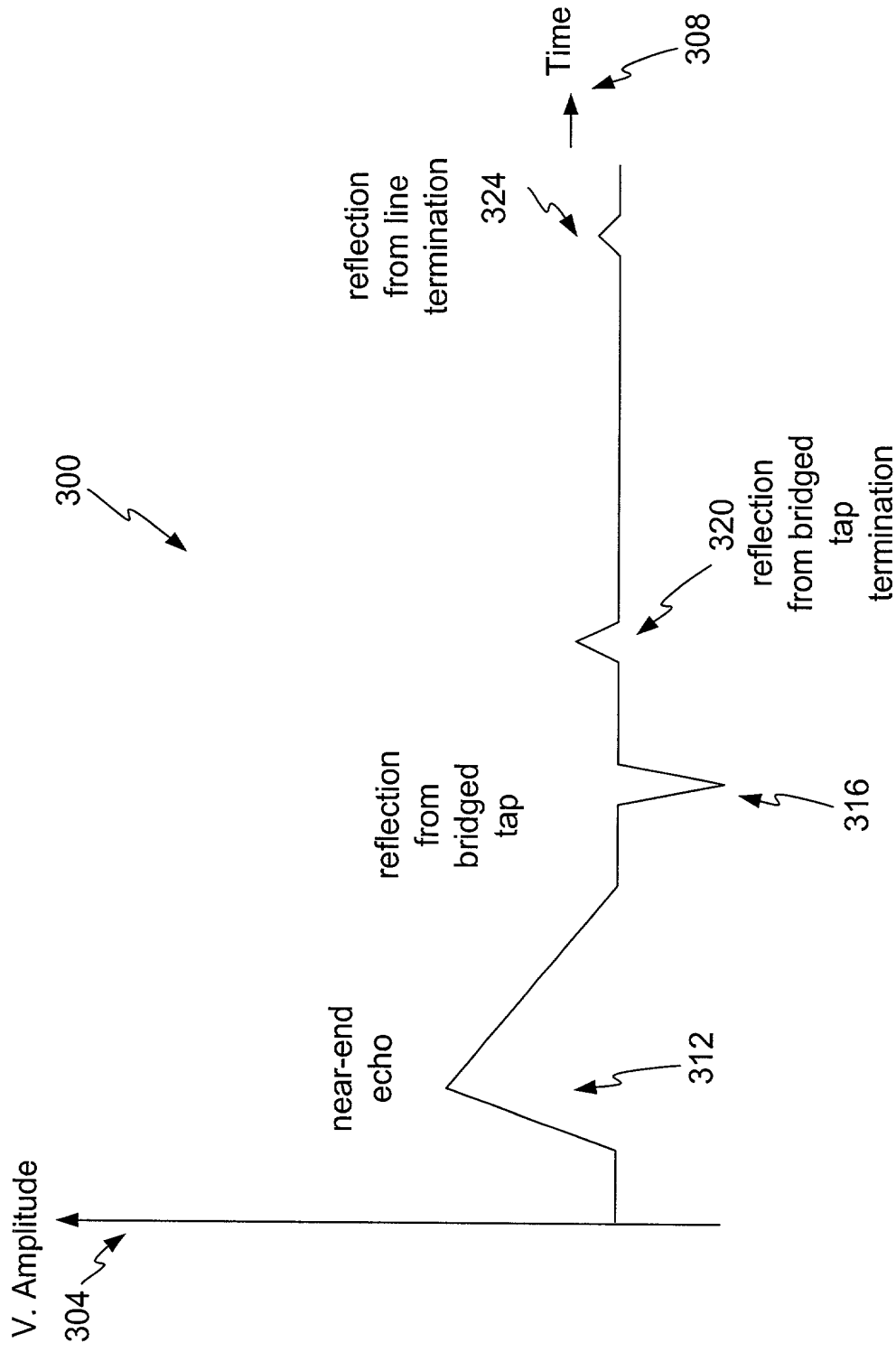


Fig. 2B

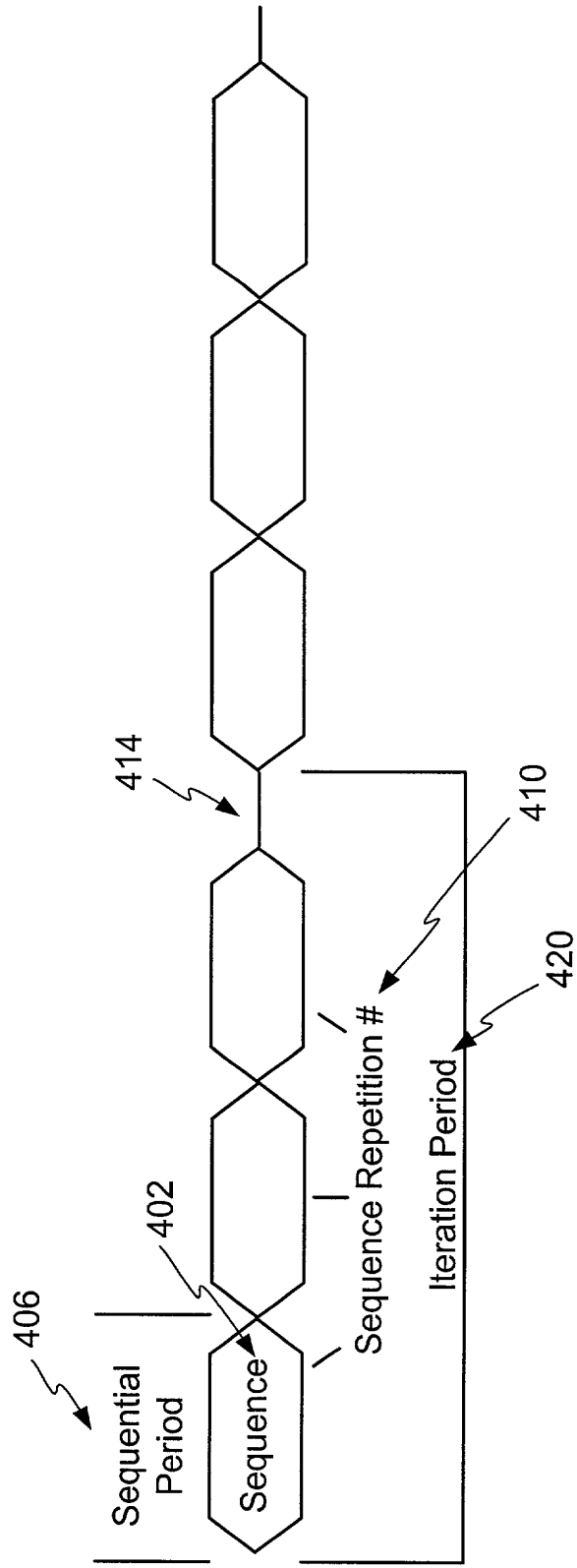


Fig. 3

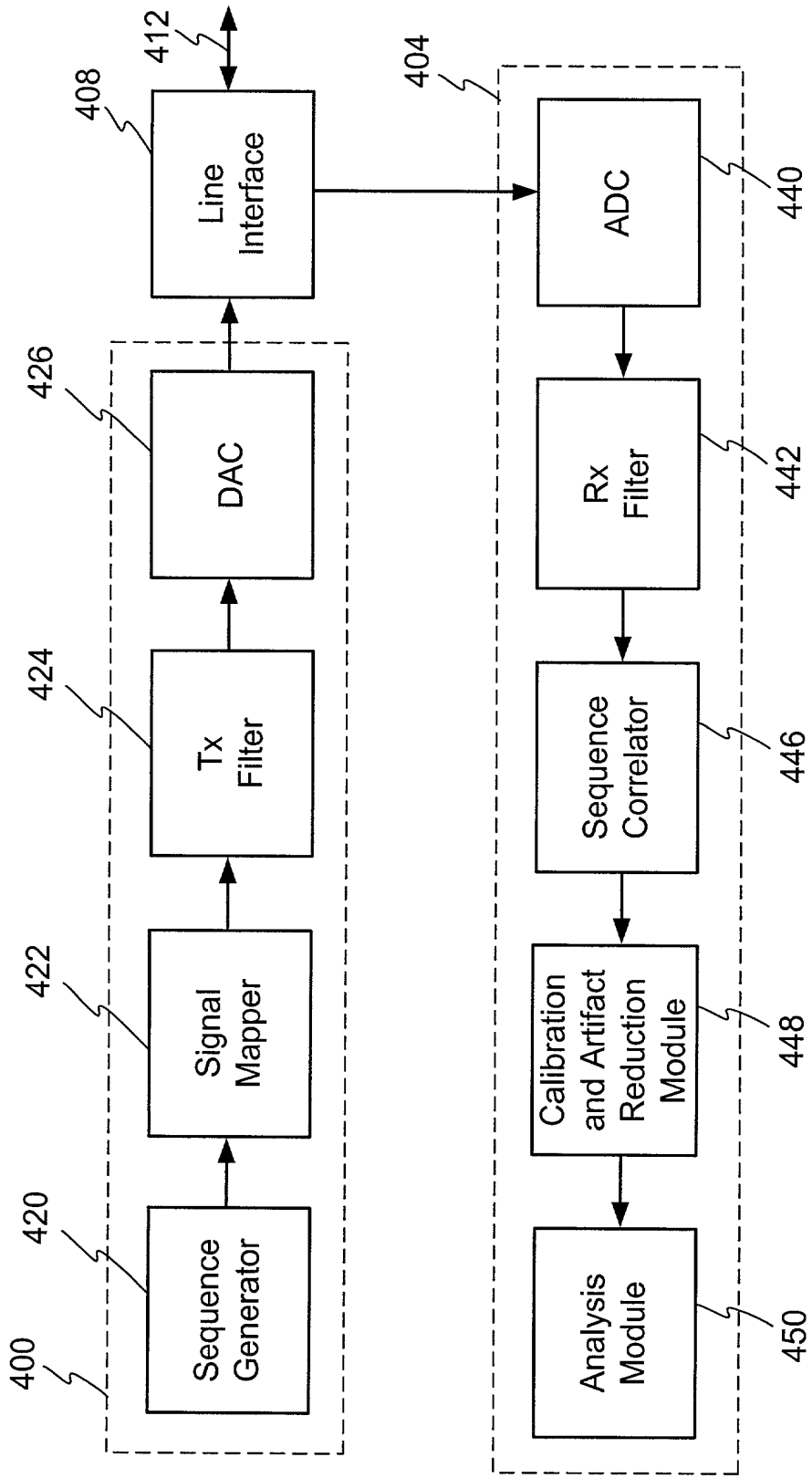


Fig. 4

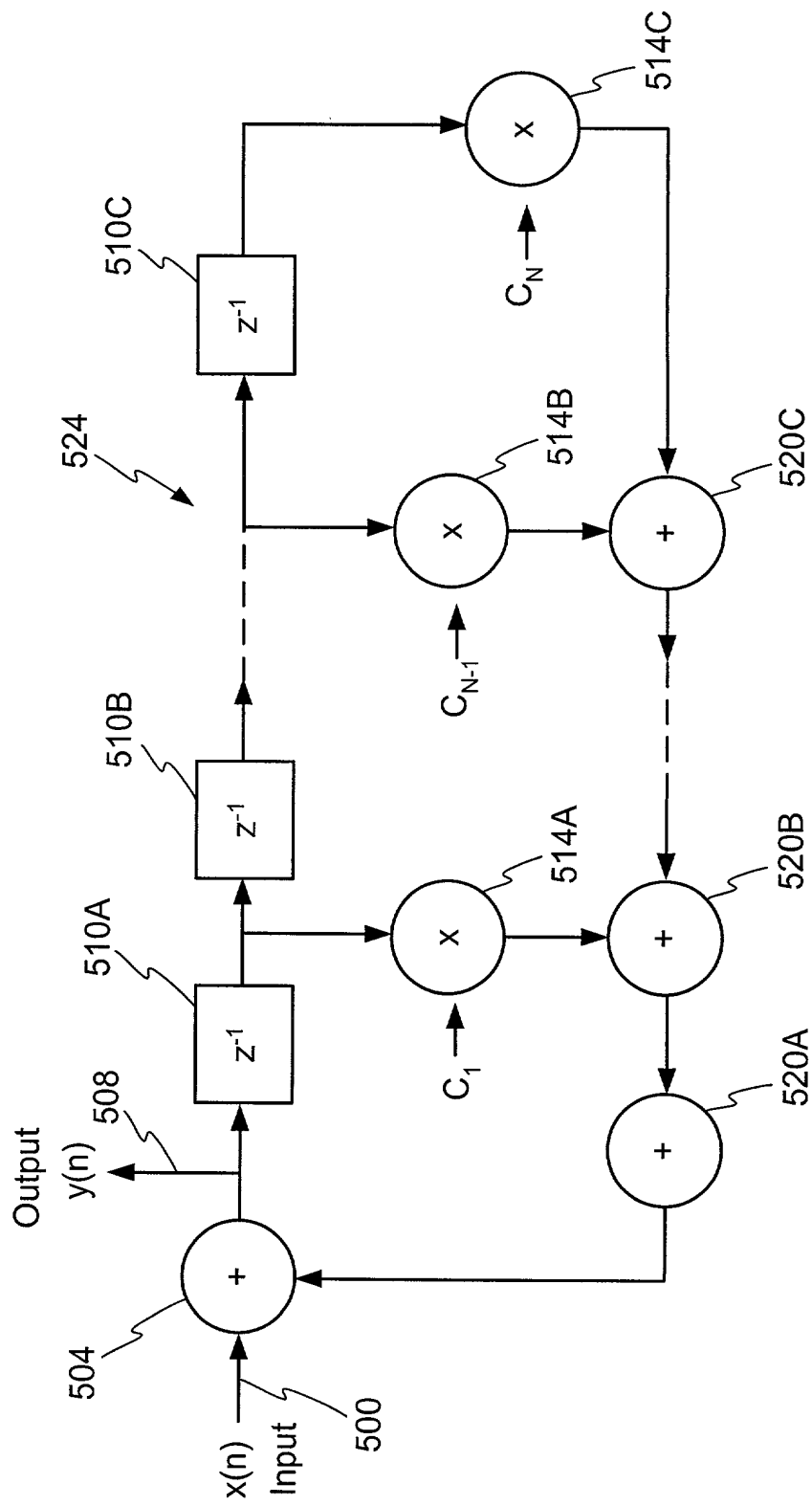


Fig. 5

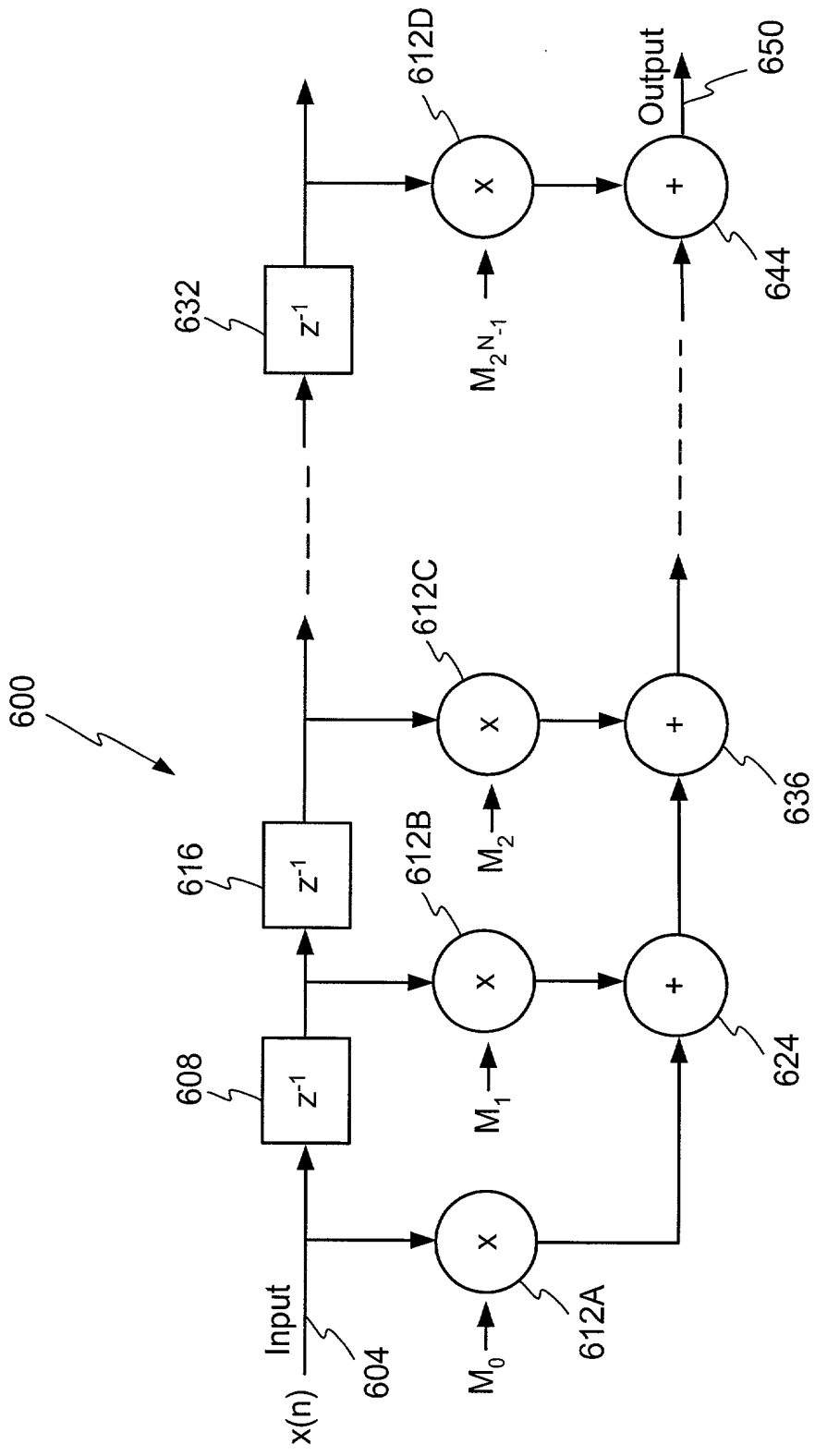


Fig. 6

FIG. 7 is a block diagram of a receiver system 700. A receive signal 704 is input to a multiplier 708. The multiplier 708 also receives a feedback signal 712, which is the output of a summing junction 718. The output of the multiplier 708 is fed into the summing junction 718, which also receives a reference signal 720. The output of the summing junction 718 is fed back to the multiplier 708 via the feedback signal 712.

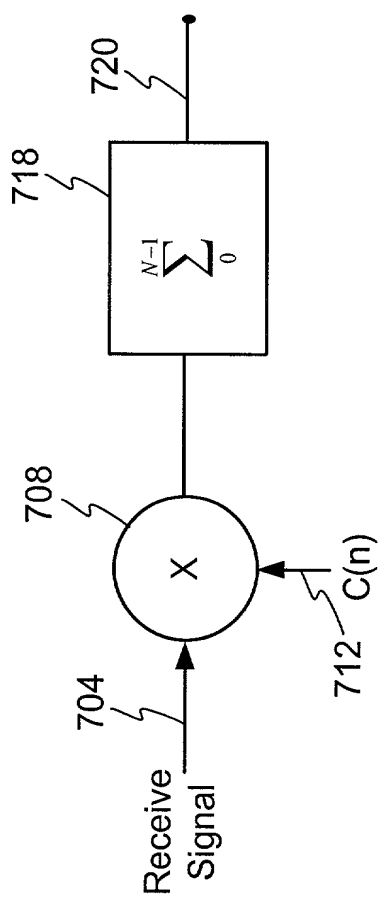


Fig. 7

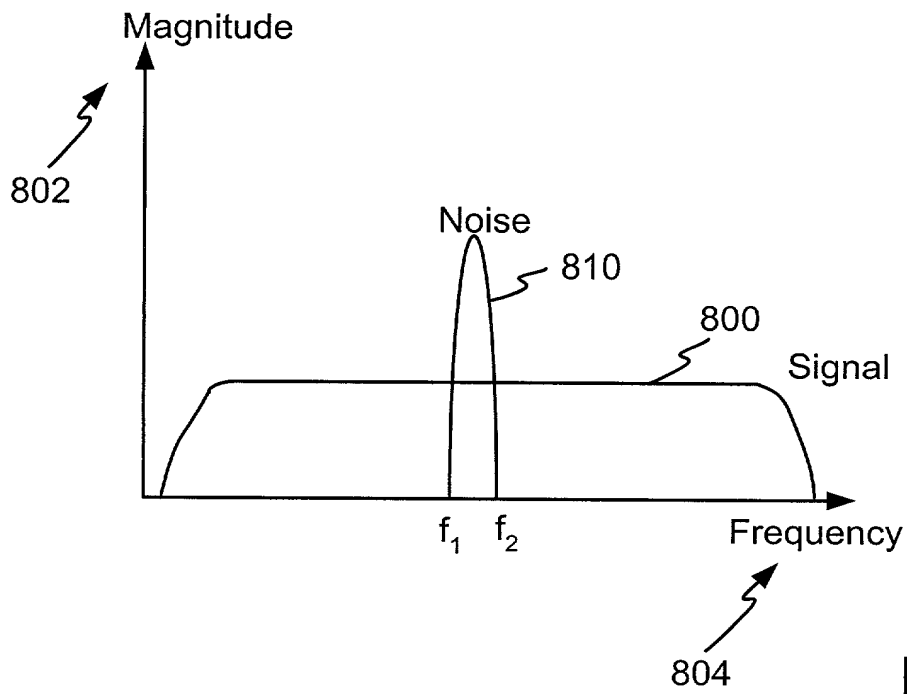


Fig. 8A

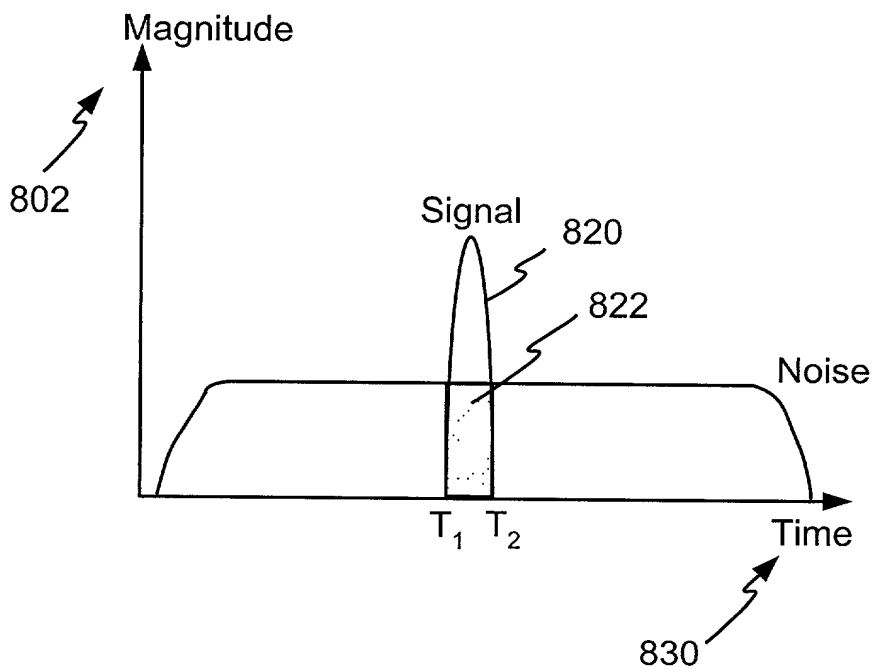


Fig. 8B

FIG. 9 is a graph of an M-sequence of length 15.

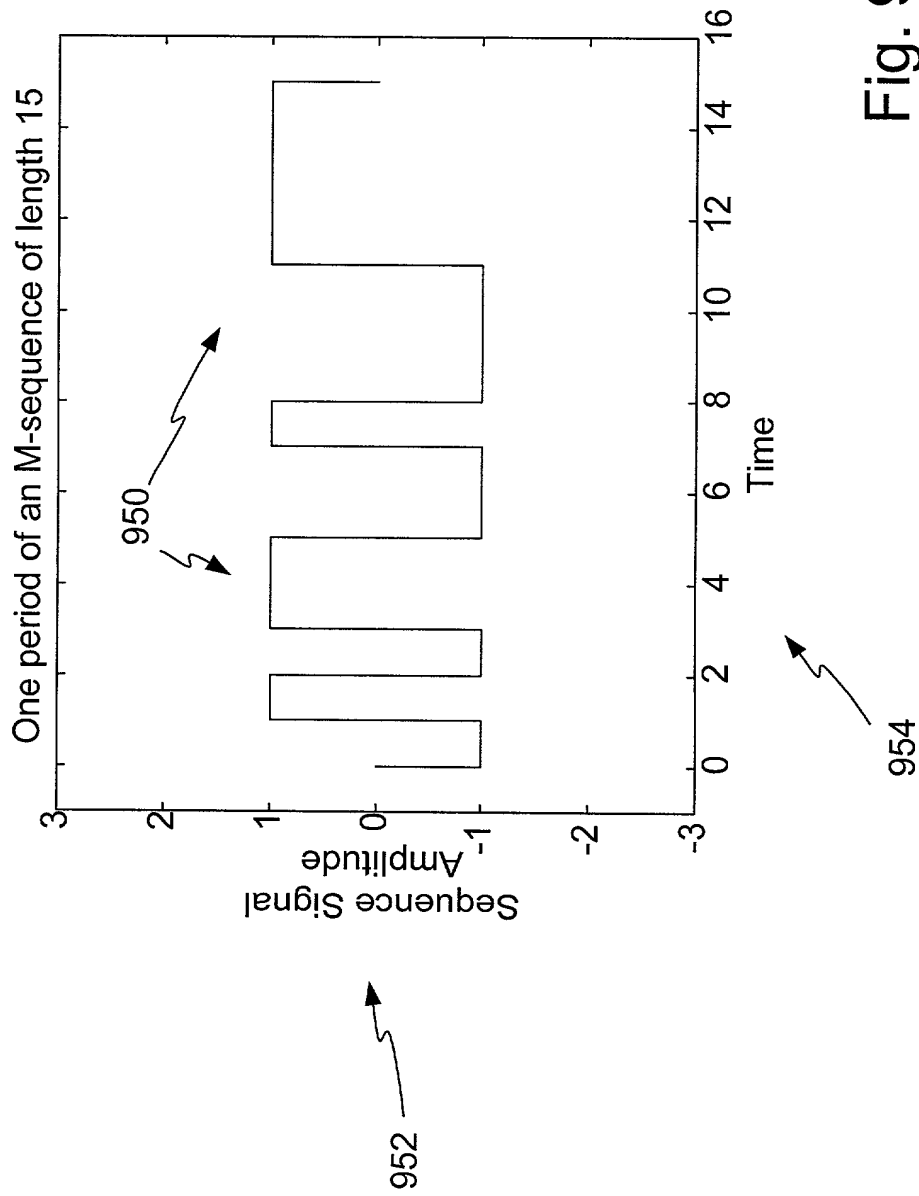


Fig. 9

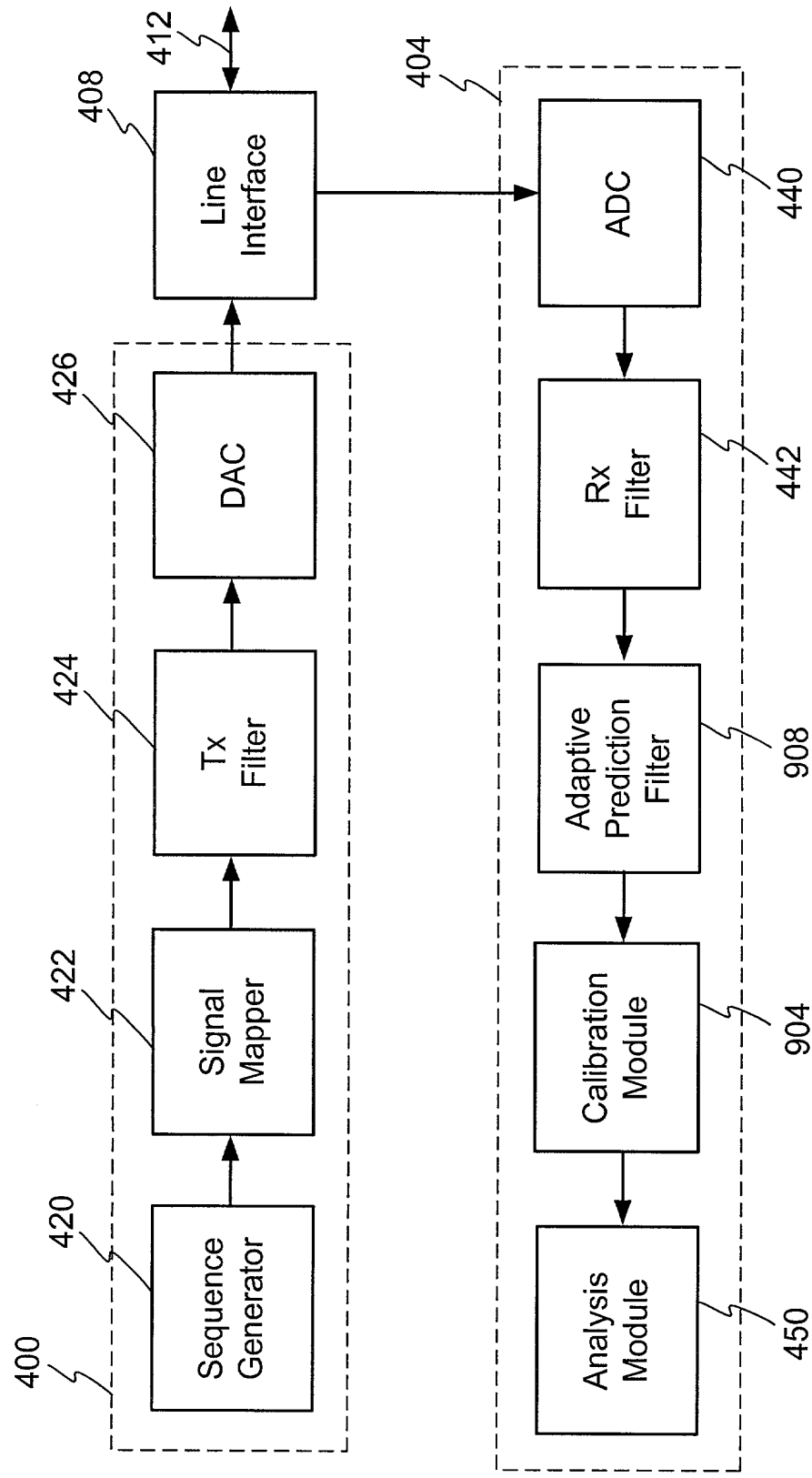


Fig. 10

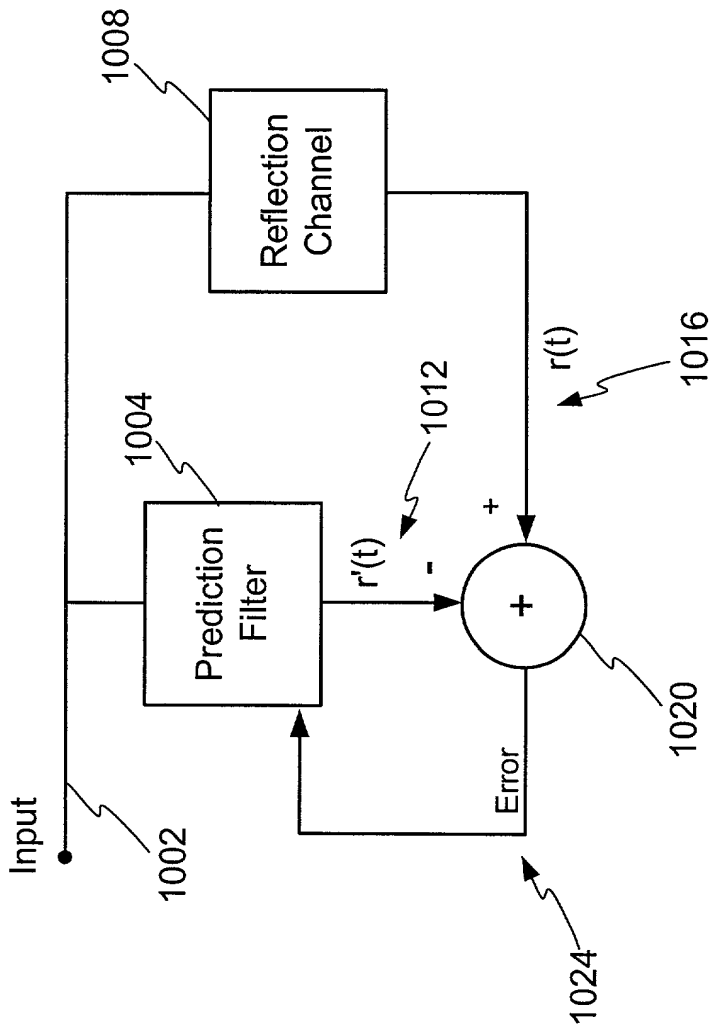


Fig. 11

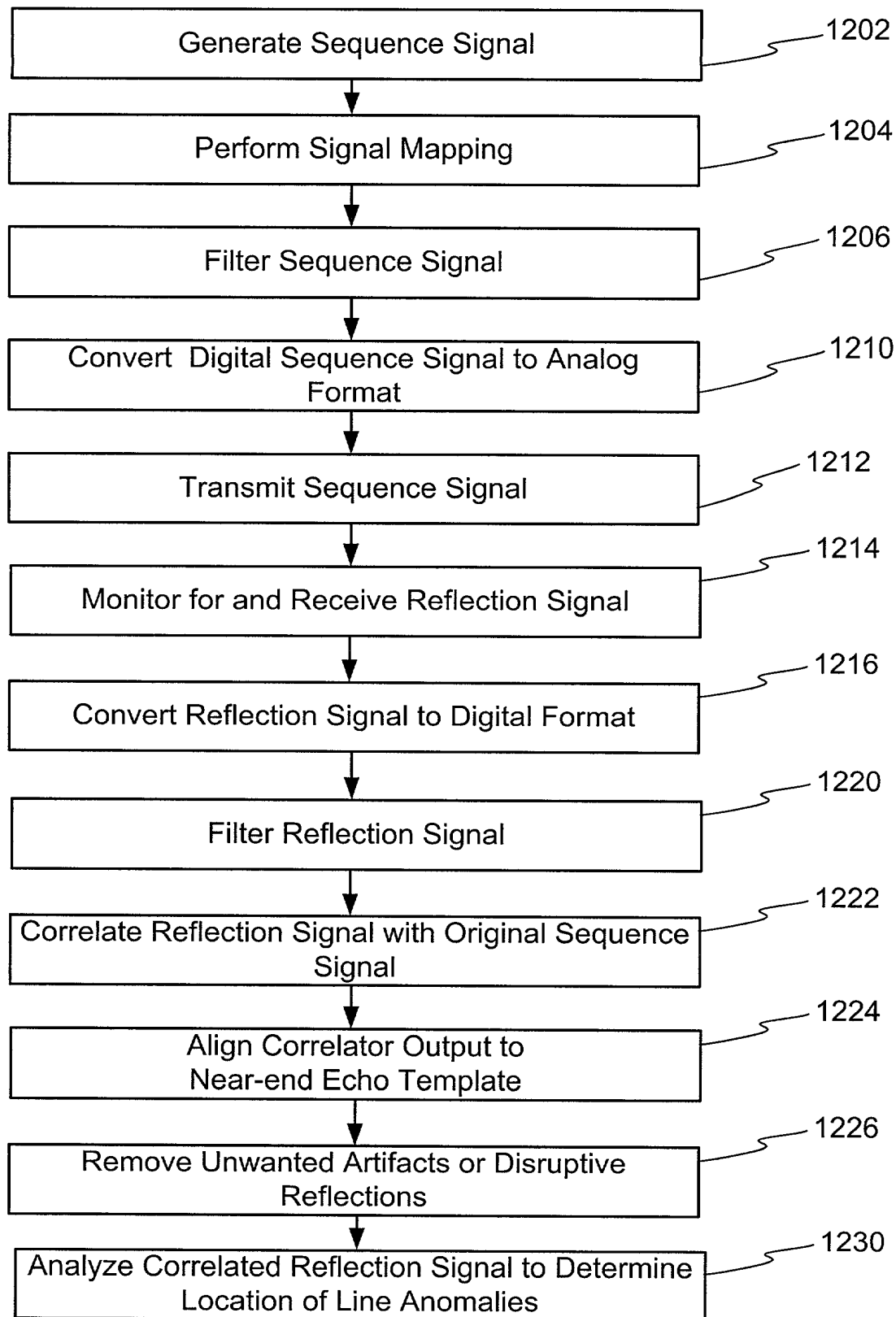


Fig. 12

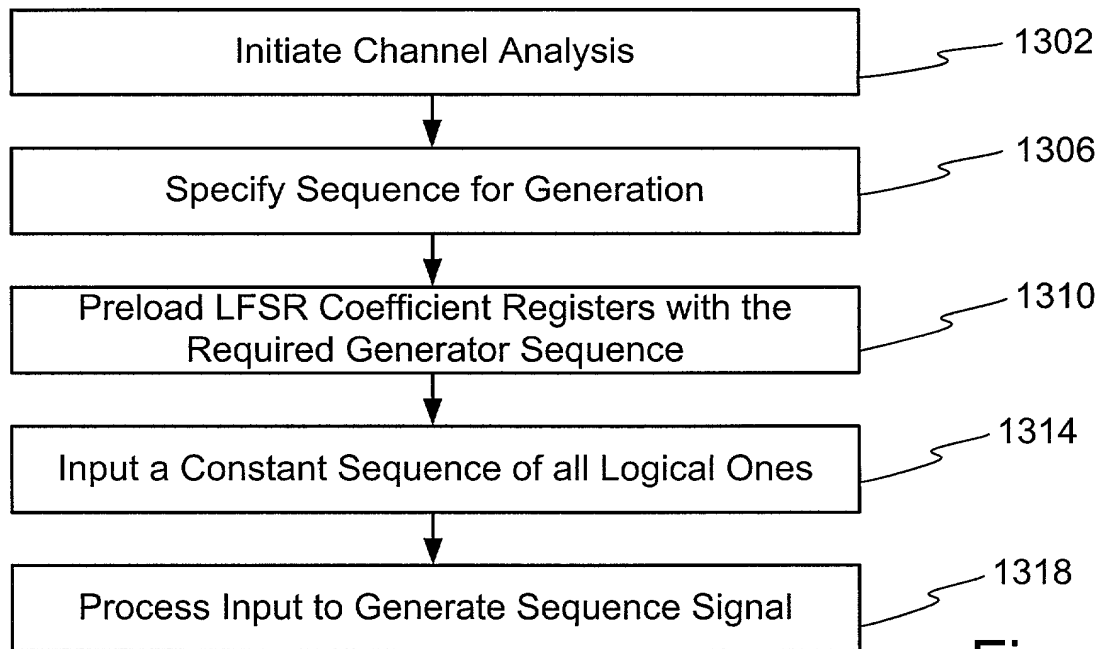


Fig. 13

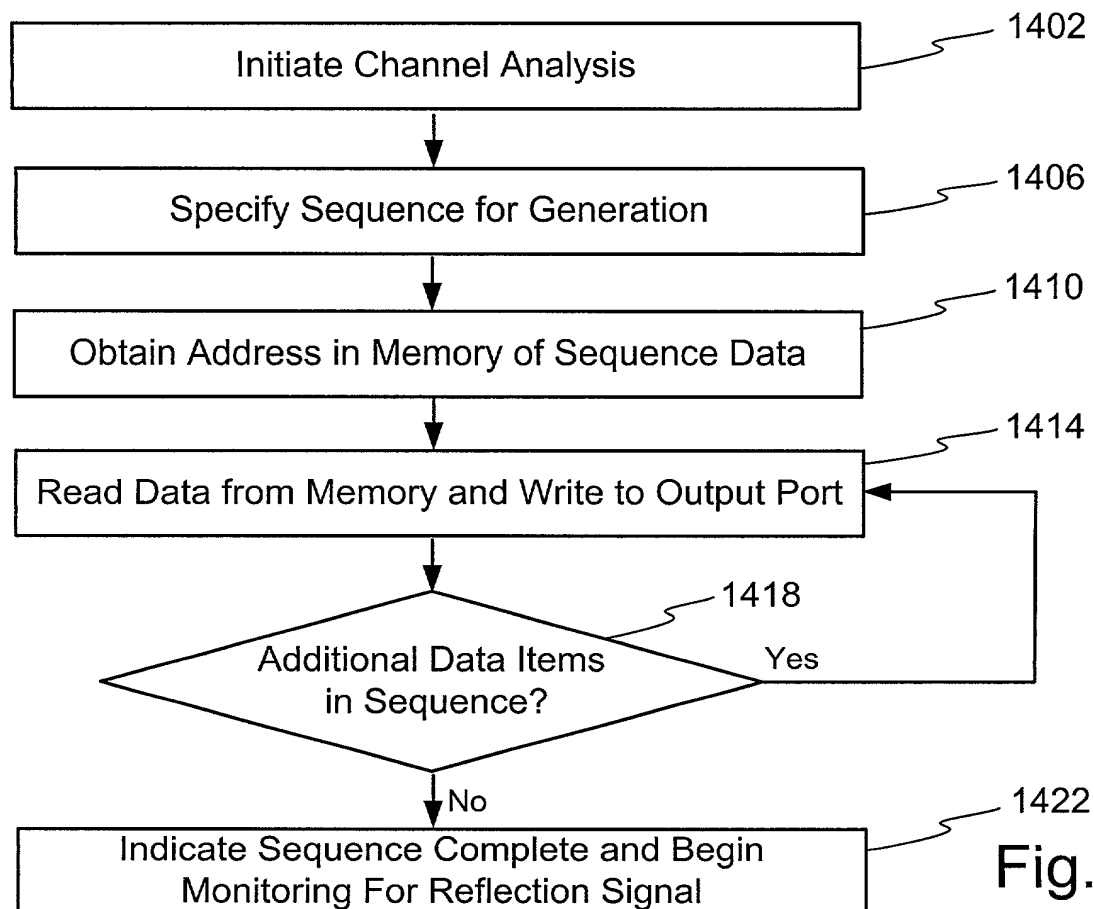


Fig. 14

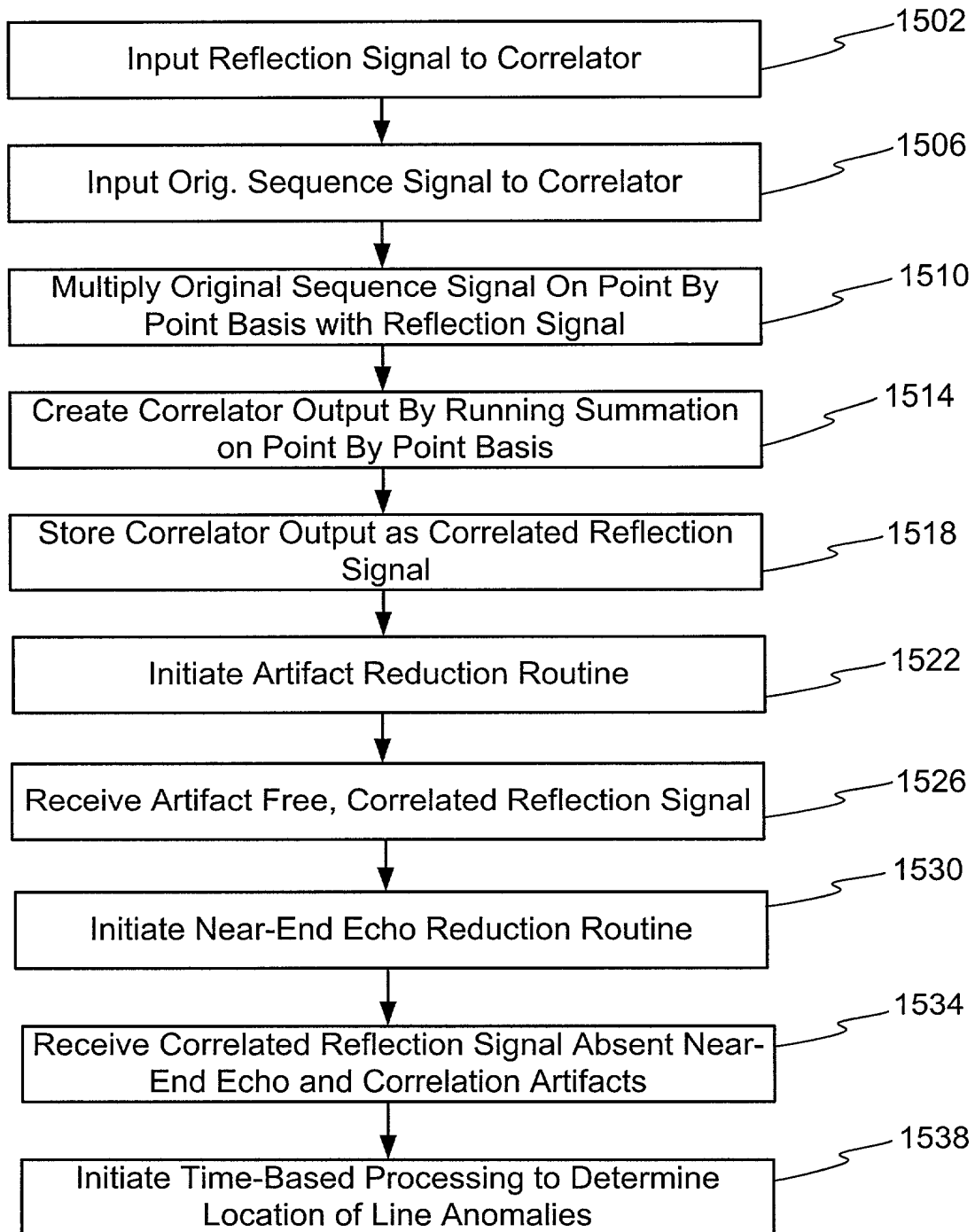


Fig. 15

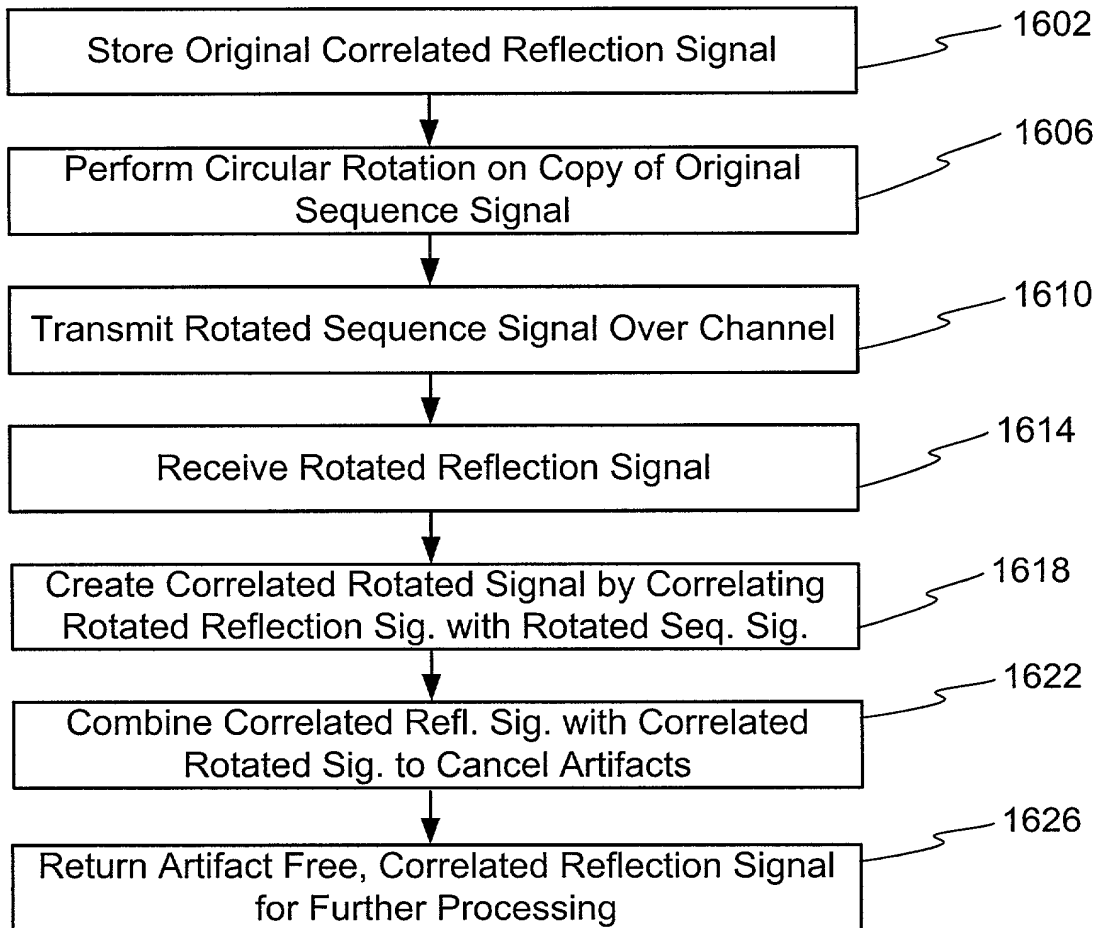


Fig. 16

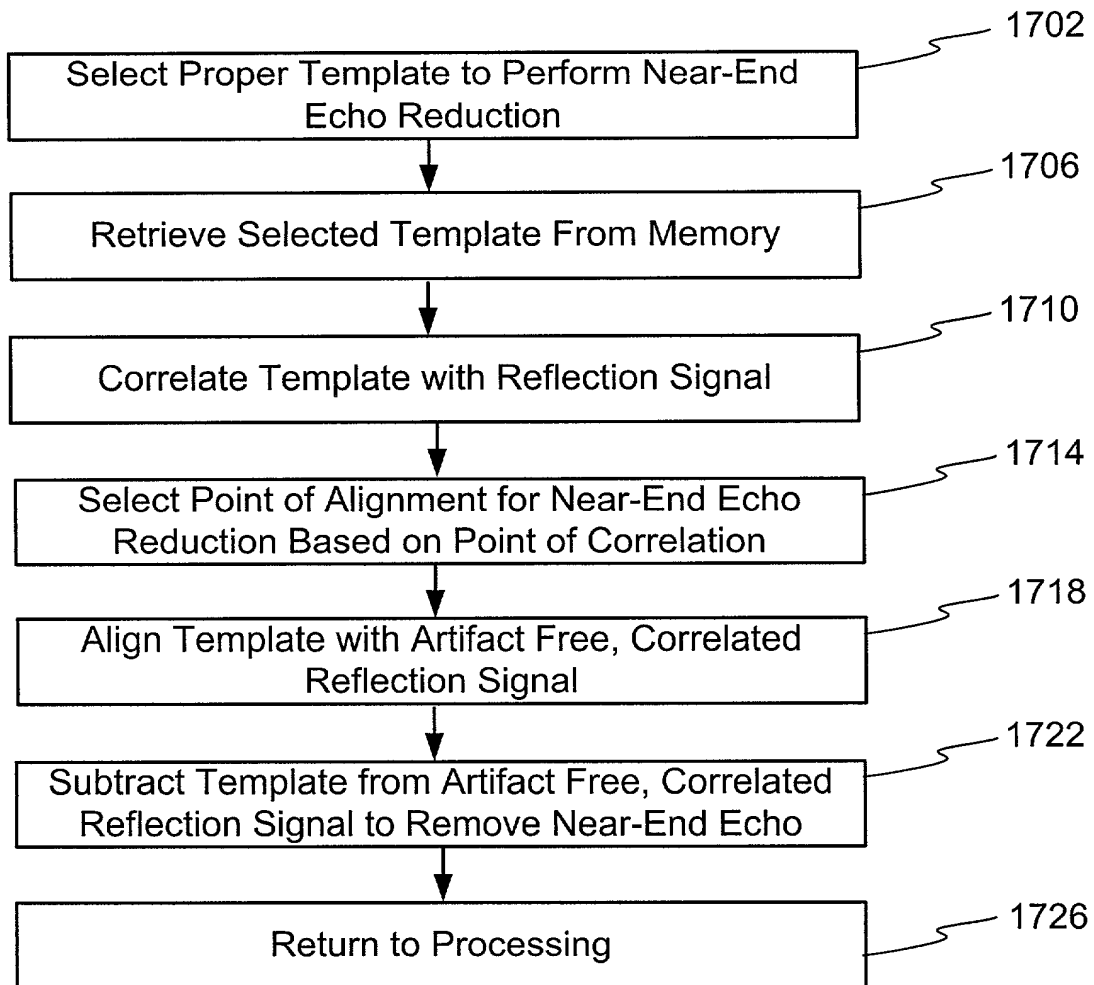


Fig. 17

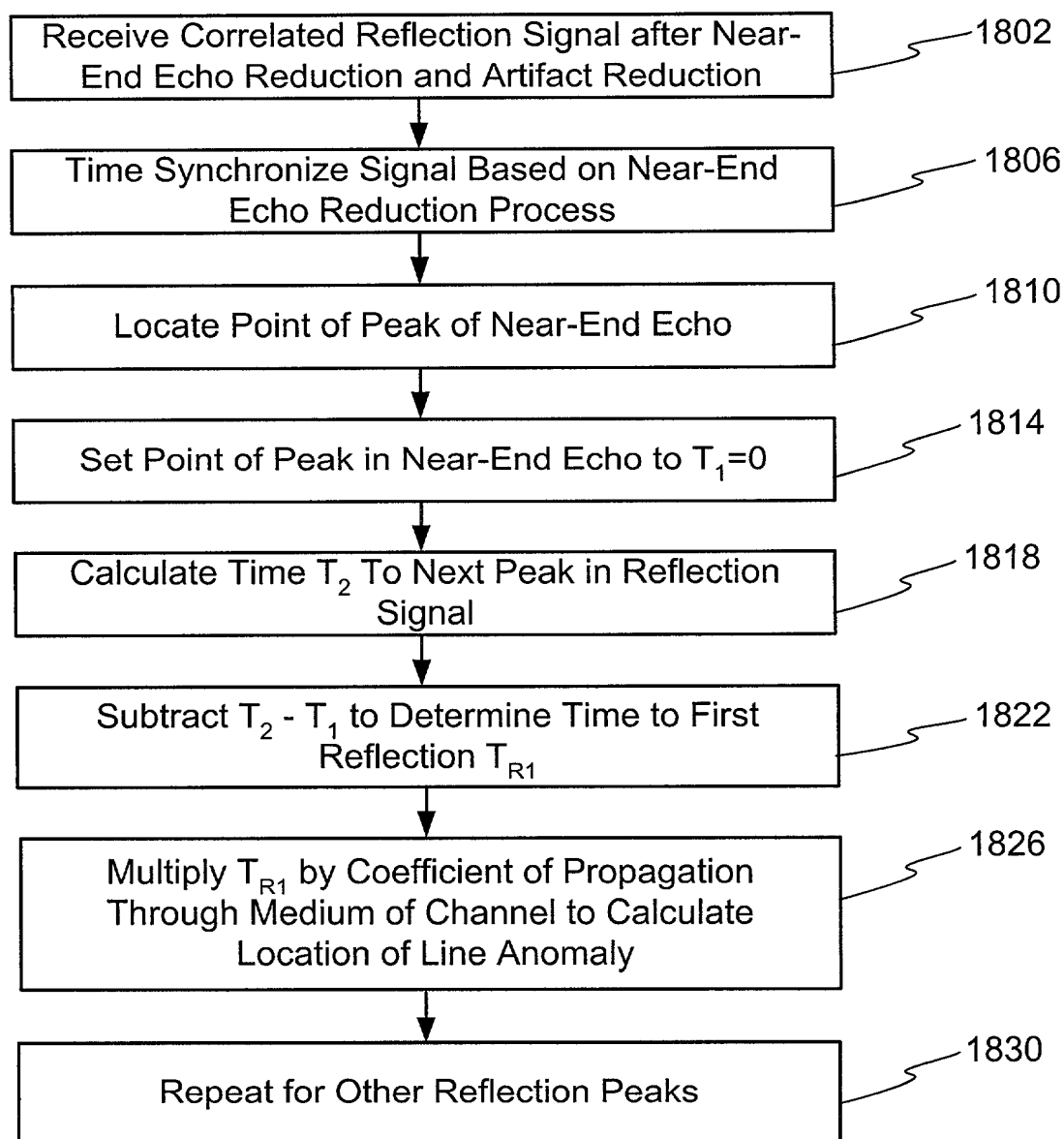


Fig. 18

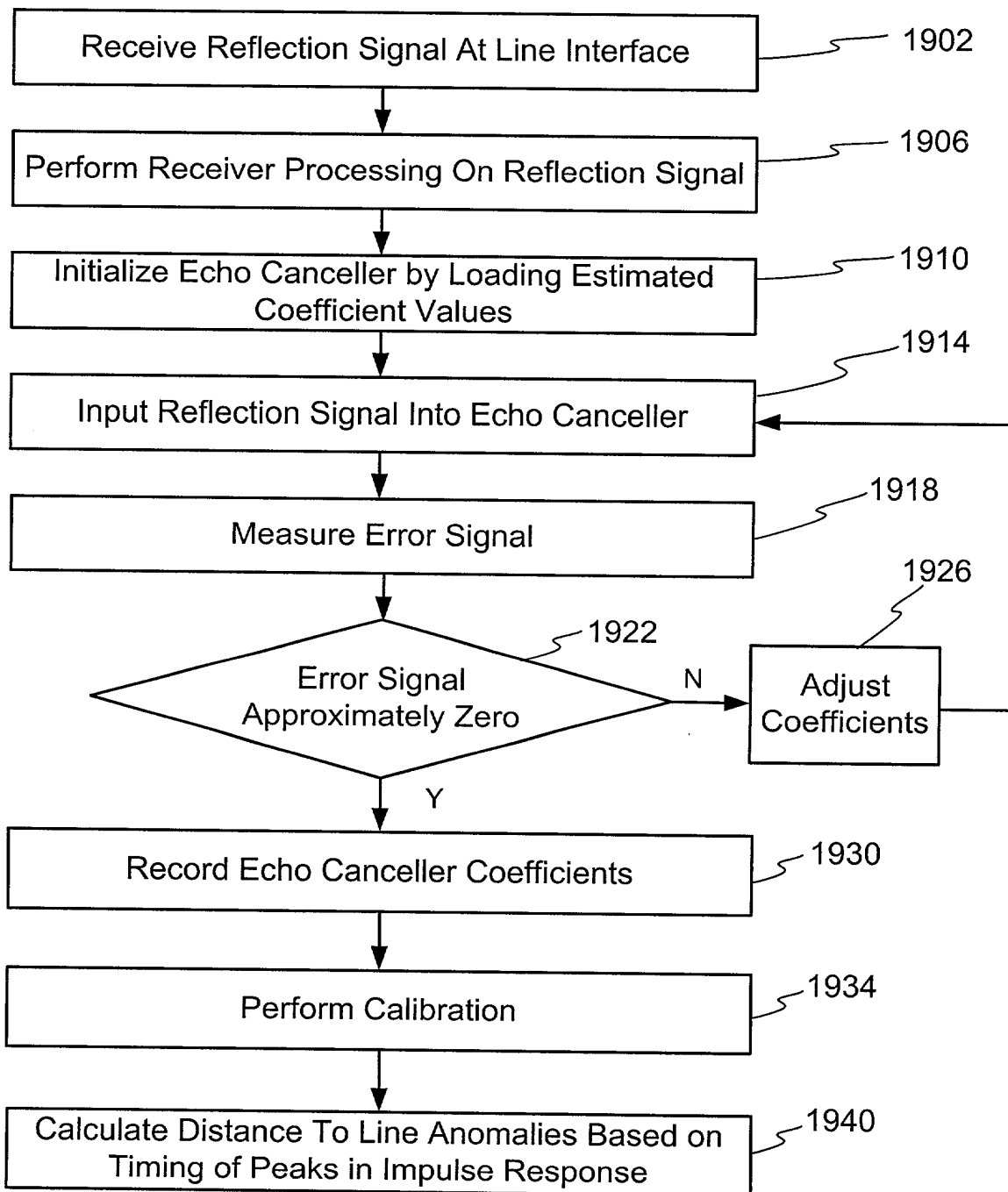


Fig. 19